



BodaHx6R

Multi-standard High-def. Decoder IP for Netbook

The Multi-Standard High-Definition Video Decoder **BodaHx6R** is the highly optimized hardware decode cores supporting **MPEG-2, H.264, VC-1(WMV9), MPEG-4, DivX(Xvid)** and designed especially suitable for UMPC or Netbook. Using a unique and optimal hardware architecture BodaHx6R enable to meet stringent power and clock requirements at low silicon cost.

— Support Standards & Compliance

Standard	Standard Compliance
H.264	ISO/IEC 14496-10 AVC(H.264) BP, MP, HP@L4.1
MPEG-4	ISO/IEC 14496-2 MPEG-4 SP, ASP@L5.0
DivX	DivX High definition profile
H.263	ITU-T H.263(Annex I, J, K, T)
VC-1	SMPTE 421M-2006 VC-1 SP, MP, AP@L3.0
MPEG-2	ISO/IEC 13818-2 MPEG-2 MP@HL

— Performance

- Resolution & Frame rate:
up to Full HD(1080p 30fps / 1080i 60fps)
- HD decoding @ 133MHz
- MJPEG decoder 32MP/s(up to 4:4:4) @ 133MHz
- Required host processor resource to run : under 1 MIPS

— Features

- Simultaneous multi-standard/multiplex decoding
- CABAC/CAVLC for H.264 MP/HP
- Built-in de-blocking, de-ringing filter and post-rotation/mirroring for post processing
- Error resilience tools
 - MPEG-4 : resync. Marker & Data-partitioning with RVLC
 - H.264 : FMO & ASO
- Fully support for DXVA(DirectX Video Acceleration) for Microsoft windows(Vista)
- DivX High Definition Profile certifiable & Full Xvid compatibility
- Fully compatible with RV8/9/10 (RMVB) specification
- Dynamic clock gating for power saving

— Interface

- Host interface : 32-bit AMBA APB interface
- External memory interface : 64-bit AMBA AXI interface
- Customized interface for optimally designed multi-master environments can be available

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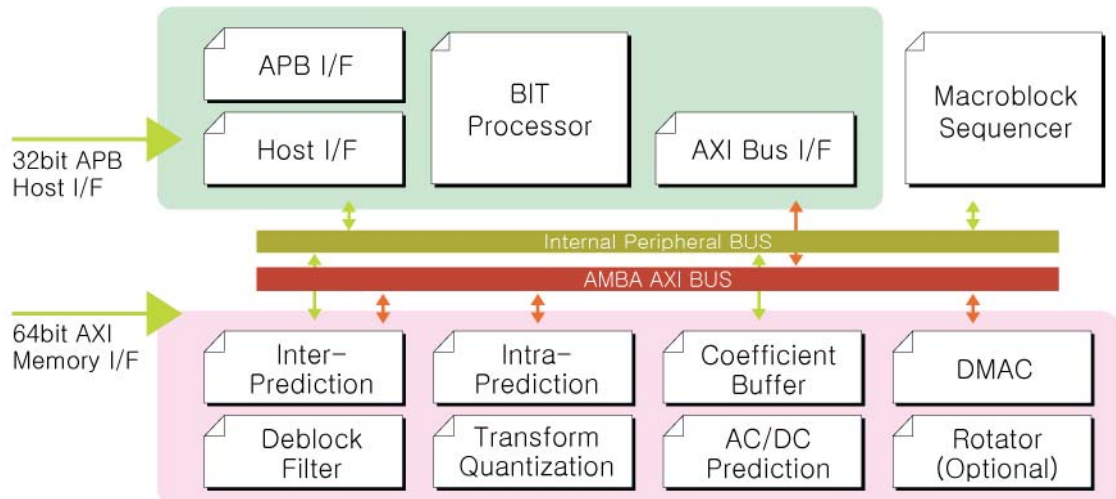


Multi-standard High-def. Decoder IP

Applications

- Ultra Mobile PC(UMPC)
- Mobile Internet Device(MID)
- Netbook

Internal Block Diagram



FPGA demonstration board Runs at 50MHz



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