



# BodaHx8

## Multi-standard High-def. Decoder IP

The Multi-Standard High-Definition Video Decoder **BodaHx8** is the highly optimized hardware decode cores supporting **MPEG-2**, **H.264**, **VC-1**, **MPEG-4(DivX)**, **RealVideo** and **(M)JPEG** to satisfy a wide range of video application from mobile to HDTV. Using a unique and optimal hardware architecture BodaHx8 enable to meet stringent power and clock requirements at low silicon cost.

### — Support Standards & Compliance

Standard	Standard Compliance
H.264	ISO/IEC 14496-10 AVC(H.264) BP, MP, HP@L4.0
MPEG-4	ISO/IEC 14496-2 MPEG-4 SP, ASP@L5.0
DivX	DivX High definition profile
H.263	ITU-T H.263(Annex I, J, K, T)
VC-1	SMPTE 421M-2006 VC-1 SP, MP, AP@L3.0
MPEG-2	ISO/IEC 13818-2 MPEG-2 MP@HL
RealVideo	RealVideo v.8/9/10(RMVB)
MJPEG	ISO/IEC 10918 JPEG Baseline

### — Performance

- Resolution & Frame rate:  
up to Full HD(1080p 30fps / 1080i 60fps)
- HD decoding @ 166MHz
- MJPEG decoder 32MP/s(up to 4:4:4) @ 133MHz
- Required host processor resource to run : under 1 MIPS

### — Features

- Simultaneous multi-standard/multiplex decoding
- CABAC/CAVLC for H.264 MP/HP
- Built-in de-blocking, de-ringing filter and post-rotation/mirroring for post processing
- Error resilience tools
  - MPEG-4 : resync. Marker & Data-partitioning with RVLC
  - H.264 : FMO & ASO
- DivX High Definition Profile certifiable & Full Xvid compatibility
- Fully compatible with RV8/9/10 (RMVB) specification
- Dynamic clock gating for power saving

# BodaHx8

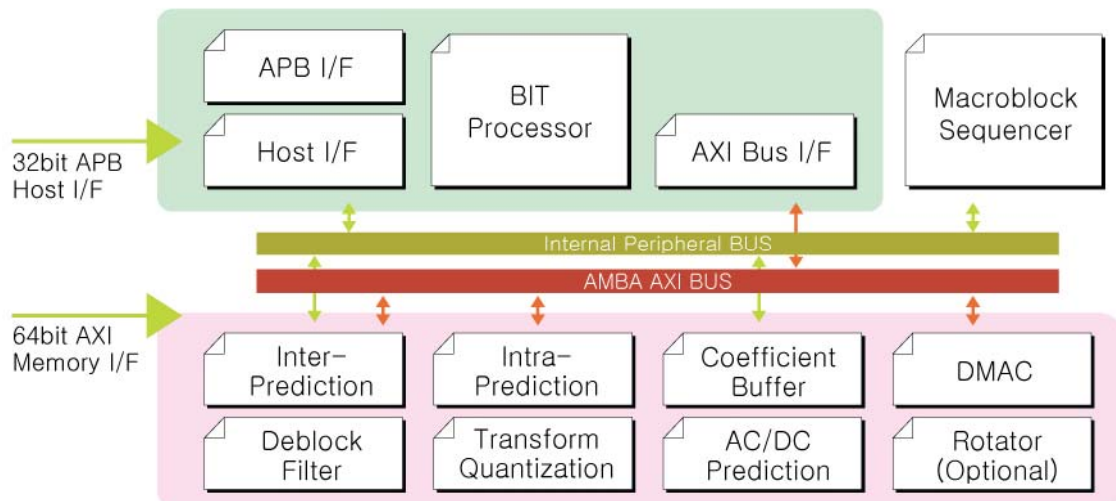


## Multi-standard High-def. Decoder IP

### Applications

- Smart Phone
- Mobile Internet Device (MID)
- Portable Multimedia Player
- Automotive
- HDTV / HD-Set top box

### Internal Block Diagram



### FPGA demonstration board Runs at 50MHz



#### +Headquarter+

#### +China Office+