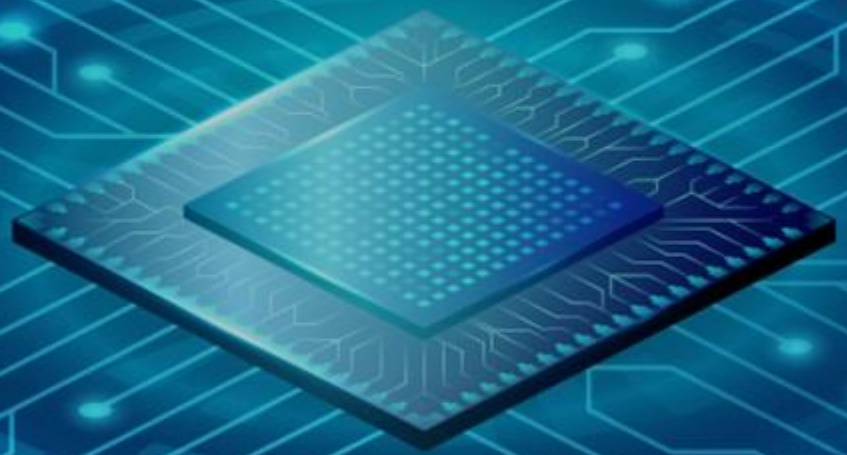
Company Overview

2021.03



Contents

Chips&Media (C&M)

Video IP Leader

More than 100+ worldwide licensees with Global Top-tier Semiconductor Manufactures

Leading semiconductor video IP
World first AV1 & HEVC Multi-standard decoder IP

Media Solution

Product Portfolio
- Video Codec
- Computer vision
- Image Signal Processing

Benefits

Product features
- High performance
- Small size
- Low power consumption
- High image quality

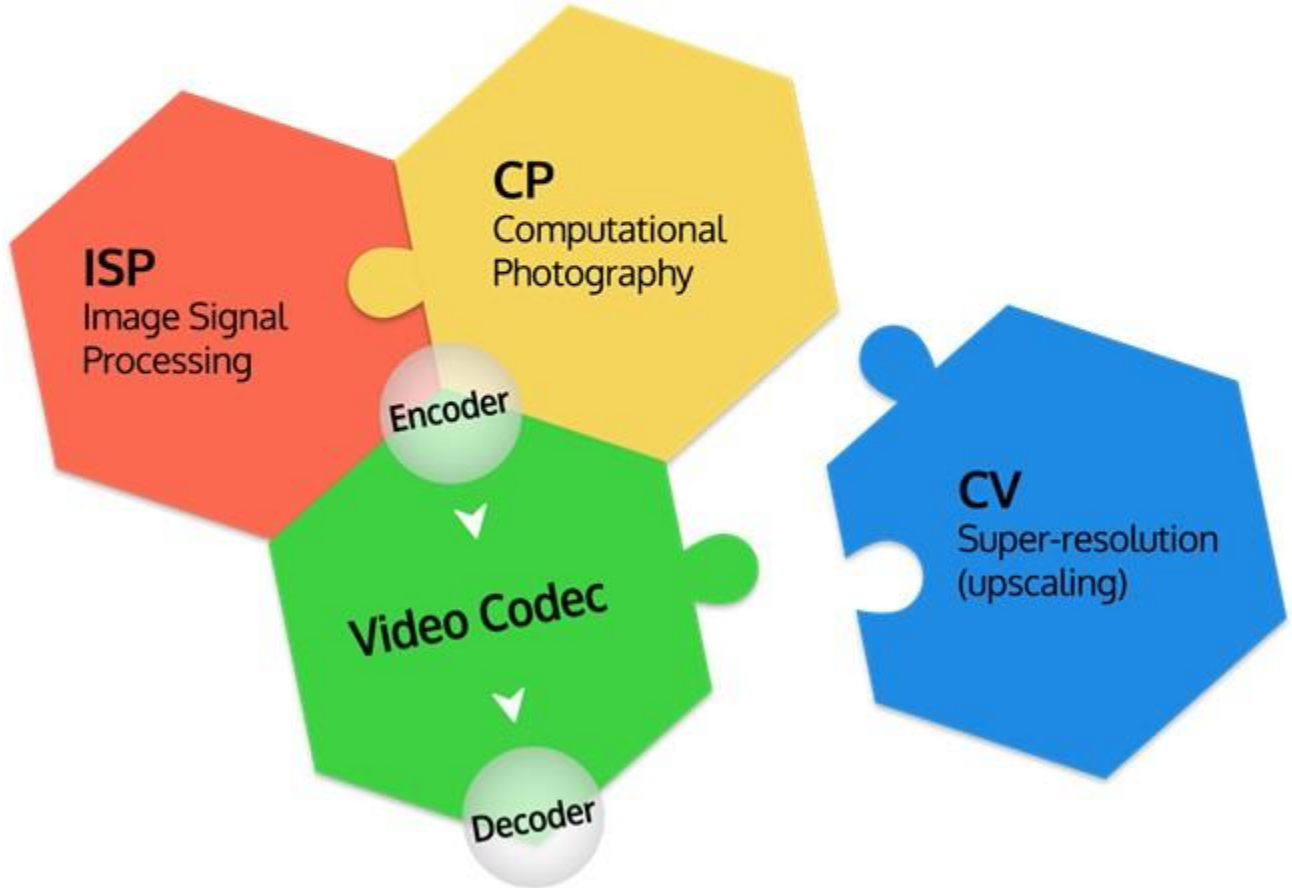
Future

Market leader
- AV1 Encoder & VVC codec IP

42% R&D investment in *three* years
- Y'18 (44%)
- Y'19(40%)
- Y'20(42%)

About Chips&Media (C&M)

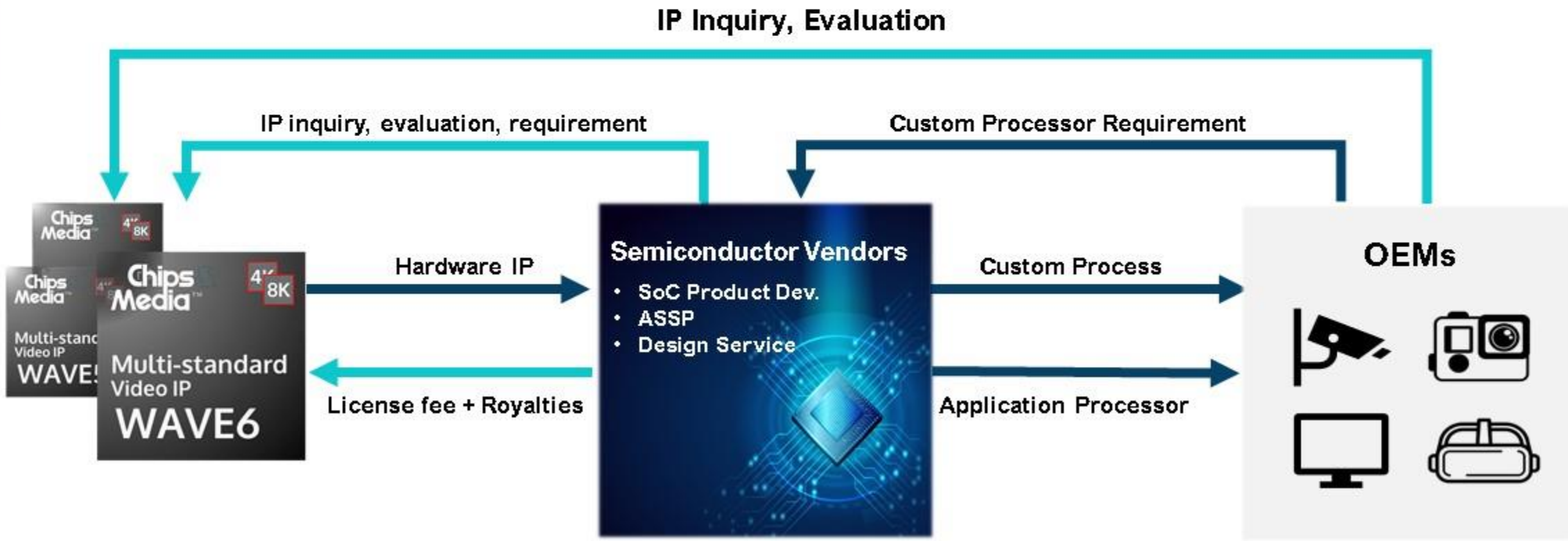
Chips&Media is a leading global semiconductor HW IP provider specializing in multimedia IPs, video codecs, deep learning-based upscaling super-resolution, and image signal processing.



Company Introduction

C&M Business Model

- **License fee - paid at delivering IP to chipset vendors**
  - Single use of IP
  - Multiple uses of IP for a handful of years
- **Running Royalties - two options available**
  - based on the percentage of chip price
  - fixed price per chip and step down by chip volumes



C&M Business Value Chain

Delivered more than 100 worldwide licensees including global top-tier semiconductor manufacturers

Chips Media™

- Fabless
- Foundry
- Design House
- Integrated device manufacturer

REALTEK, SiliconMotion, XIK, NXP, NOVATEK, CAVIUM NETWORKS, SUNPLUS, nuvoTon, altek, ITE, ENTROPIC, VIXS, Actions, ICT, AMD, MPRC, HISILICON, ZTE中兴, Silan, SOLOMON SYSTECH, Hisense, SAMSUNG, GOKE, artosyn, alhua, SYNOCHIP, SPREADTRUM, BITMAIN, LG Electronics, RDA

Company Introduction

C&M History & Leadership

2020
Released Super-resolution IP

2015
Listed in KOSDAQ

2014
Released UHD Video codec IP

2008
Spin-off C&M Micro; appointed Steve Sang-Hyun Kim as CEO

2003
Chips&Media, Inc. founded,
Corporate R&D Center established

Steve Kim
(CEO)

Gus Lee
(CFO)

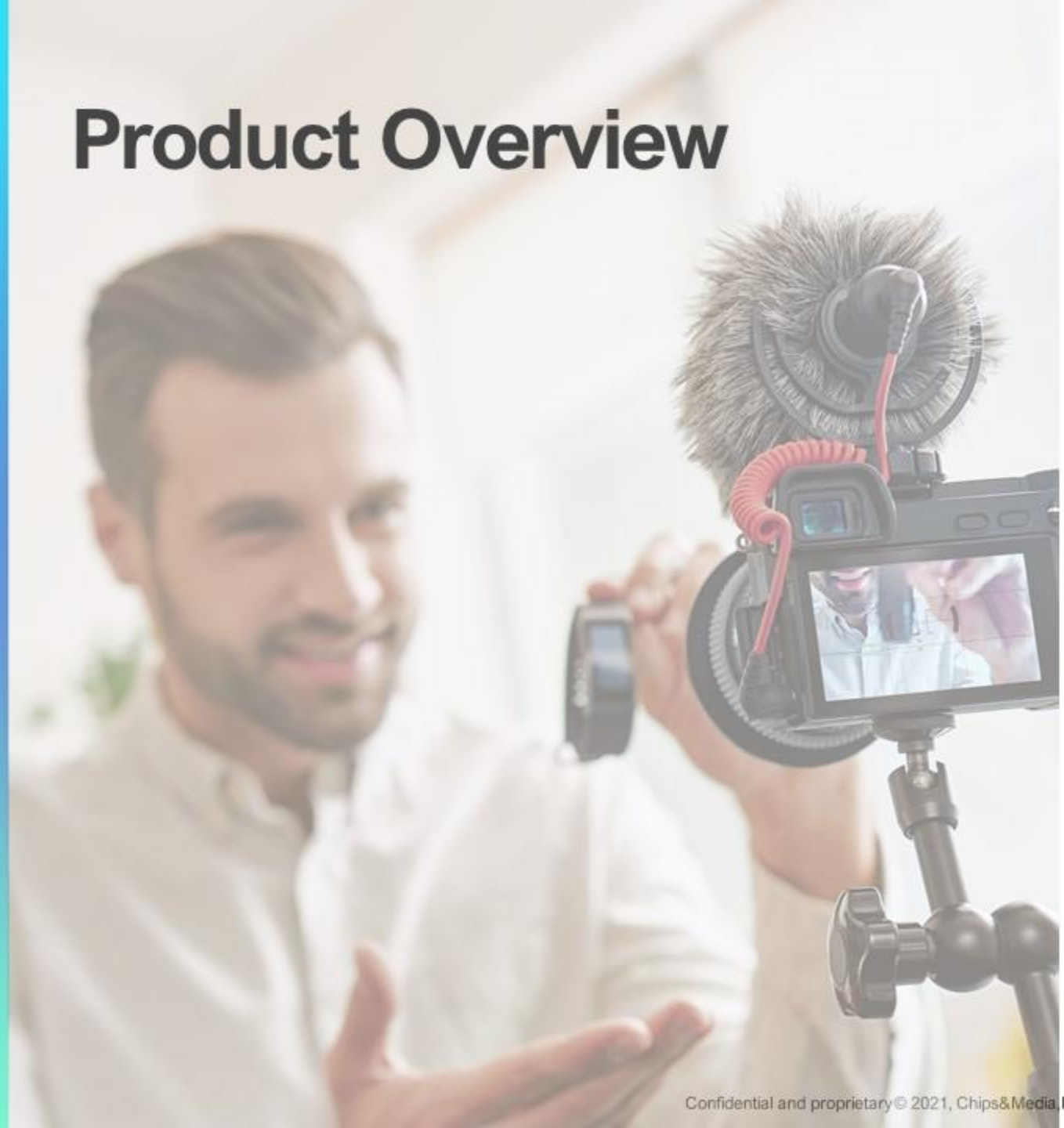
Jeff Oh
(CTO)

Tedd Kang
(CHRO)

Product overview

- **Video codec**
- **Computer vision**
- **Image processing**

Product Overview



Video Codec

- Multi-standard Video IP
- HEVC, H264, AV1, VP9, AVS2, …
- CODA/BODA, WAVE4, WAVE5 and WAVE6

Computer Vision

- Deep learning-based neural network
- Super-resolution as upscaling

Image Signal Processing

- 2MP, 5MP, 8MP and 13MP ISP
- 3D noise reduction, high dynamic range, lens distortion correction

Effective and Efficient Video Codec IPs

Offers accelerated video codec HW IP cores with an optimized competitive PPA (Power, Performance, Area) that supports the multi-standard video codec up to 8Kp60fps.

	CODA/BODA	WAVE4	WAVE5	WAVE6
Supported Standard	AVC/H.264 and other legacy	HEVC/H.265, VP9	AV1, HEVC/H.265, AVC/H.264, VP9, AVS2	VVC, HEVC/H.265, AVC/H.264, AV1
Target performance	2Kp60@266MHz	4Kp30@400MHz	4Kp60@500MHz 8Kp60@1GHz	4Kp60@500MHz


Surveillance / STB


Drones/Robotics


Automotive


AR·MR ·VR


Mobile/Tablets


Home entertainment

Video Codec

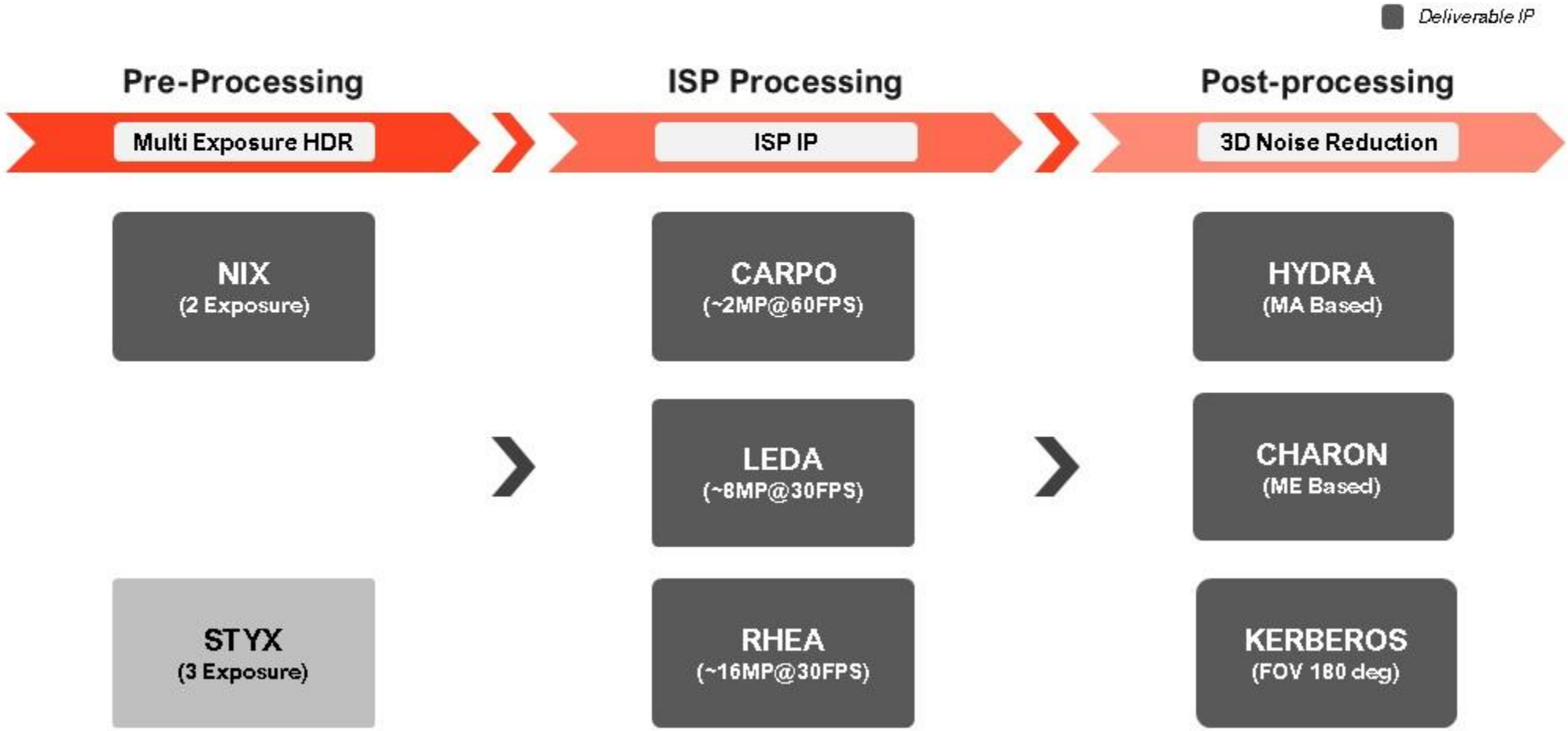
Revolutionary Super Resolution

Discover that latest innovative, fully hardwired deep learning-based inference super-resolution HW IP that reconstructs a higher resolution image or sequence from the observed low-resolution in real-time.

Super Resolution

A Wide Range of Image Signal Processing

Introducing an end-to-end full-featured ISP IP that converts that sensor's signal into a better visible and processable format while providing user-centric customization for more flexible requirements and configuration capabilities.



Thank You

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